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Stone Pillar Technologies to Demonstrate Stone Pillar Suite for Technology Development During 2006 IEDM

Demonstrations will highlight automated test chip, testing, and report generation capabilities for managing process and device experimentation

SANTA CLARA, California, November 28, 2006 – Stone Pillar Technologies Inc. will exhibit the automation and process improvement capabilities for technology development made possible by their Stone Pillar Suite at the San Francisco Hilton during IEDM 2006. Demonstrations and briefings will be available at an open suite to be held at the **Seacliff Room** in the **Executive Conference Center** at the **San Francisco Hilton** during the following times:

December 10: 5:00pm – 8:00pm

December 11: 11:00am – 9:00pm

December 12: 11:00am – 9:00pm

Hors d'oeuvres, coffee, and soft drinks will be provided with a wine and beer reception each evening at 6:00pm.

Demonstrations and briefings will highlight how Stone Pillar Suite supports early-stage technology development through yield improvement including new capabilities for automating test chip generation, testing and report generation.

Stone Pillar Suite bridges design and manufacturing by interconnecting data, facilitating detailed analyses, and providing insight to improve development and manufacturing processes, performance, and yield. Visitors will see demonstrations of software tools that automate parametric test chip creation, speed test plan creation, and automate data analysis and report generation. Find out how Stone Pillar tools help customers reduce errors in technology development and win in the race to create the best technologies.

For additional details, visit www.stonepillar.com or contact Stone Pillar Technologies at (408) 748-1105.

About Stone Pillar

Stone Pillar Technologies, Inc. is the leading provider of data management systems for semiconductor process technology development. Stone Pillar Suite enables semiconductor manufacturing and design engineers to gain valuable insight into design-for-manufacturability issues from technology development through yield improvement.

Stone Pillar Suite automates tasks such as design rule creation and documentation; test chip development, documentation, and revision; test plan creation; and data management, analysis, and report generation. Stone Pillar Suite reduces engineering costs and capital expenses and speeds the development of new process technologies.

Stone Pillar software is in use by leading semiconductor vendors and at leading research institutions worldwide. For additional details, visit www.stonepillar.com or contact Stone Pillar Technologies at (408) 748-1105.

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