



For Immediate Release

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Stone Pillar Technologies announces initial release of Silicon Insight™ Parameterized Layout Module

New tool is integrated with Silicon Insight database and provides flexible, automated test chip layout capabilities

San Jose, California – April 8, 2003: Stone Pillar Technologies Inc., the leading provider of software solutions for semiconductor technology development, today announces the initial release of the Parameterized Layout module of the Silicon Insight software tool suite. The parameterized layout toolset provides augmented capabilities for test chip layout, easy support for test chip revisions, and full support for javascript based library development.

“The Silicon Insight Parameterized Layout module provides dramatic speedups for test chip development,” said Tony Clark, CTO for Stone Pillar. “The parameterized layout toolset makes revisions and design rule changes extremely rapid.”

For pricing or additional details, visit www.stonepillar.com or contact Stone Pillar Technologies.

About Silicon Insight

Silicon Insight is a complete toolkit for semiconductor technology development. Silicon Insight includes intelligent, automated capabilities for design rule creation, documentation, and verification, test chip development, documentation, and revision, and complete data management for semiconductor technology development and monitoring.

Silicon Insight offers a set of application tools tailored to semiconductor process and circuit development and maintenance which enable automation and optimization of common tasks and provide unique functionality to improve engineering efficiency.

All Stone Pillar products are Java based and can be deployed on virtually any hardware supporting the Java Virtual Machine.

About Stone Pillar Technologies, Inc.

Stone Pillar Technologies, founded in 1998, is the leading provider of complete data management systems for semiconductor process technology development. Silicon Insight reduces the requirement for experimental split lots by making information about experiments available and transparent across an organization.

Stone Pillar software tools enable engineers engaged in the integration, characterization, and testing of analog and mixed-signal semiconductor processes to focus on analysis, rather than tedious and error-prone tasks of gathering and formatting data.

Stone Pillar software is in use by leading semiconductor vendors and at leading research institutions worldwide.

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